

Ujwal Shirode

Ujwal Shirode is Assistant professor @ Pimpri Chinchwad College of Engineering, Technical area where mentor to startups is - Mechatronics, Sensors and Actuators and from Business point of view domain -Patent, Copyrights and Its commercialization.

Awarded by Akhil Maharashtra Prabhodan Society as “Young Researcher” on 07th Feb 2016 at Pune.

Low power SRAM cell design using Schmitt Trigger in 90nm and 180nm Technology in Hspice Simulation Tool

Funded project on “Low power operational amplifier using deep submicron technology” by University Grant Commission of Savitribai Phule Pune University.

Chief Organizer of workshop on “Design & Simulation of low power integrated circuits using H-SPICE at Deep Submicron Technology” sponsored by University of Pune under QIP (Quality Improved Program)

Completed Project on SRAM cell structures 6T, 8T, 9T, 10T have been simulated and compared on the basis of Static Noise Margin, Power Analysis and Delay Analysis using H spice environment in 90nm and 130nm CMOS technology

Completed project on Microcontroller based Expressway Management System” having features of Street Light Control, Speed Control, Pollution Control and Toll Control has been demonstrated using microcontroller 89c51 interfaced with 2*16 LCD and 4*4 keypad for user interaction

Chief Guest lecturer on “commercialization of patents” by department of information and technology, PCCOE, Pune. 2nd March 2015.

Chief Guest lecturer on “Research methodology and its ways of implementations” by computer science department, PCCOE, Pune on 26th Dec 2016.

National level conference presentation on "Wi-Max security and Protocols" at government college of engineering, Jalgaon, 2010

Attend 2nd International conference on "VLSI Design" at Pune, 2012